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APPLICANTS  
 Minari Arai, Tokyo, JAPAN;

\*\* CONTINUING DATA *None*

\*\* FOREIGN APPLICATIONS *Yes*  
 JAPAN 2002-354993 12/06/2002

IF REQUIRED, FOREIGN FILING LICENSE GRANTED  
 \*\* 03/09/2004

Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance Verified and Acknowledged Examiner's Signature <i>[Signature]</i> Initials <i>[Initials]</i>	STATE OR COUNTRY JAPAN	SHEETS DRAWING 11	TOTAL CLAIMS 4	INDEPENDENT CLAIMS 4
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TITLE  
 Semiconductor integrated circuit device and method of detecting delay error in the same

FILING FEE  RECEIVED 856	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees ( Filing ) <input type="checkbox"/> 1.17 Fees ( Processing Ext. of time ) <input type="checkbox"/> 1.18 Fees ( Issue ) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit
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